

Test 2: Monday 4/5, 2:00 PM - 3:15 PM (test available 1:45 PM) (3:15 – 3:30 PM to scan and submit)

- Design of combinational circuits
 - Circuit conversion to all NAND or NOR gates
- Design and use of
 - Multiplexers
 - Demultiplexers
 - Decoders
 - Encoders
- Design of combinational circuits using PLD's
- Latch and flip flops characteristics and timing.
- Design of ripple counters
- Analysis of sequential circuits

Via WebEx with your camera pointing to you and your work area. Alternatively, You can use your phone to show work area.

